

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of	Docket No: Q79582
Kouhei NADEHARA	Group Art Unit: 2435
Appln. No.: 10/764,504	Examiner: Randal D. Moran
Confirmation No.: 9262	Patent No.: 7,809,132
Filed: January 27, 2004	Issue Date: October 5, 2010
For: IMPLEMENTATIONS OF AES ALGORITHM FOR REDUCING HARDWARE WITH IMPROVED EFFICIENCY	

REQUEST FOR CERTIFICATE OF CORRECTION

ATTN: Certificate of Correction Branch

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to the provisions of 37 C.F.R. § 1.322, please enter the attached Certificate of Correction.

Since the errors noted are believed to be the fault of the Patent and Trademark Office, we are not enclosing the \$100.00 Certificate of Correction fee. If it is found to be to the contrary, please charge our Deposit Account No. 19-4880.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

Respectfully submitted,

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23373

CUSTOMER NUMBER

Date: December 3, 2010

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO: 7,809,132
DATED: October 5, 2010
INVENTOR(S): Kouhei NADEHARA

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11, Line 42: Delete "S_{2,2}," and insert -- S"_{2,2}, --

Column 14, Line 3: After "S_{2,2}," insert -- S_{1,2}, --

MAILING ADDRESS OF SENDER:

SUGHRUE MION, PLLC

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23373

CUSTOMER NUMBER

PATENT NO. 7,809,132

No. of additional copies

None